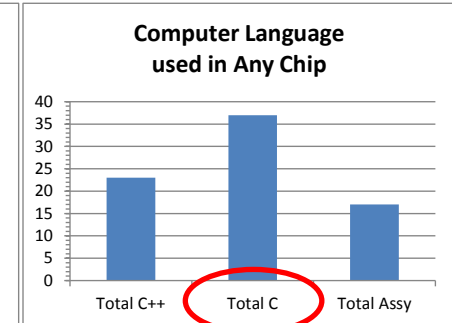
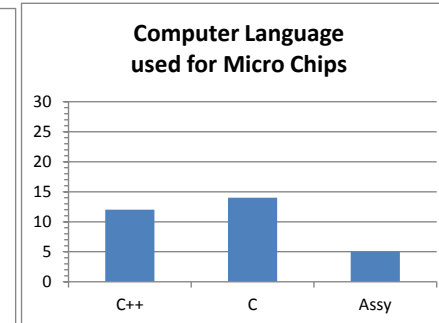
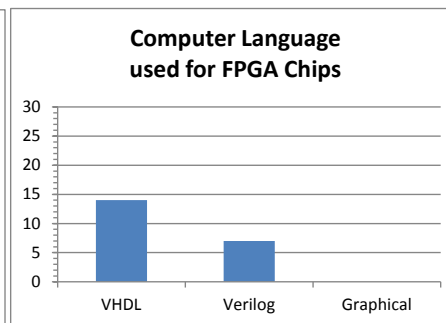
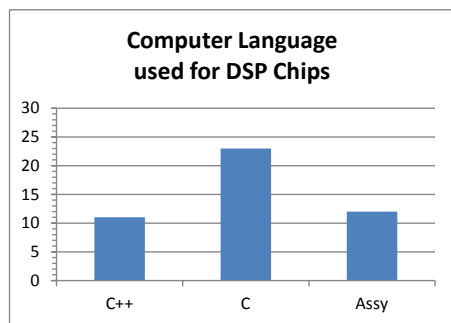
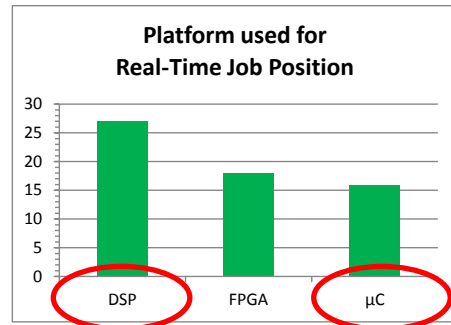


Real-Time Embedded Systems Job Opening Requirements Survey as of 31Jan2013								
Total number of job openings looked at: 45 (Jul-Dec 2010); xx (Jan-Apr 2011); yy (May-Feb 2011); 22 (Sep 2012-Jan 2013)								
DSP			FPGA			Microcontroller		
27			18			16		
C++	C	Assy	VHDL	Verilog	Graphical	C++	C	Assy
11	23	12	14	7	0	12	14	5

DSP FPGA μ C
27 18 16

Total C++	23
Total C	37
Total Assy	17



Notes:

1. Jobs searched on Monster as follows:

2. Search keywords Jul-Dec 2010 Calgary = DSP Electronic Hardware Firmware Algorithm Real-Time Simulation

Search keywords Jul-Dec 2010 BC = electrical electronic design engineer algorithm simulation digital signal processing DSP ASIC firmware real time operating system modem system

Search keywords Jul-Dec 2010 Canada = DSP

Search keywords Jan-Apr 2011 Canada = electrical electronic design engineer algorithm simulation digital signal processing DSP ASIC firmware real time operating system modem system

Search keywords May-present 2013 = real-time AND DSP

Search keywords May-present 2013 = real-time AND FPGA

Search keywords May-present 2013 = real-time AND electrical